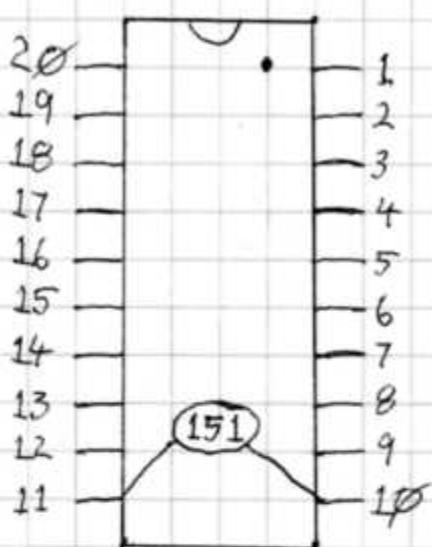
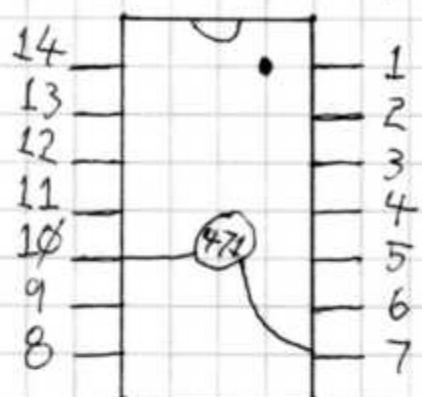


IC24 ('373)

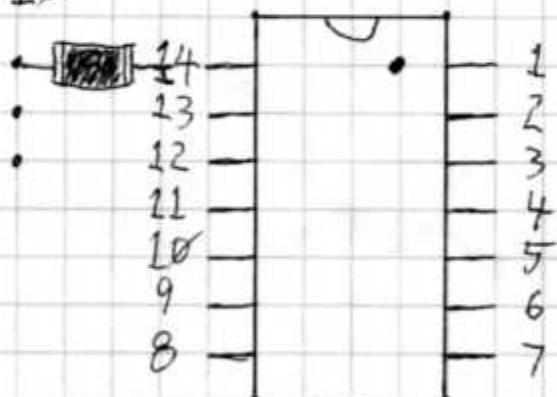


IC8 ('08)

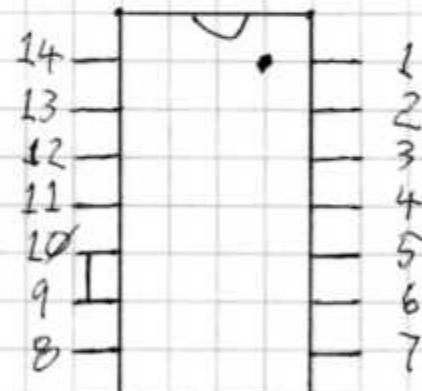


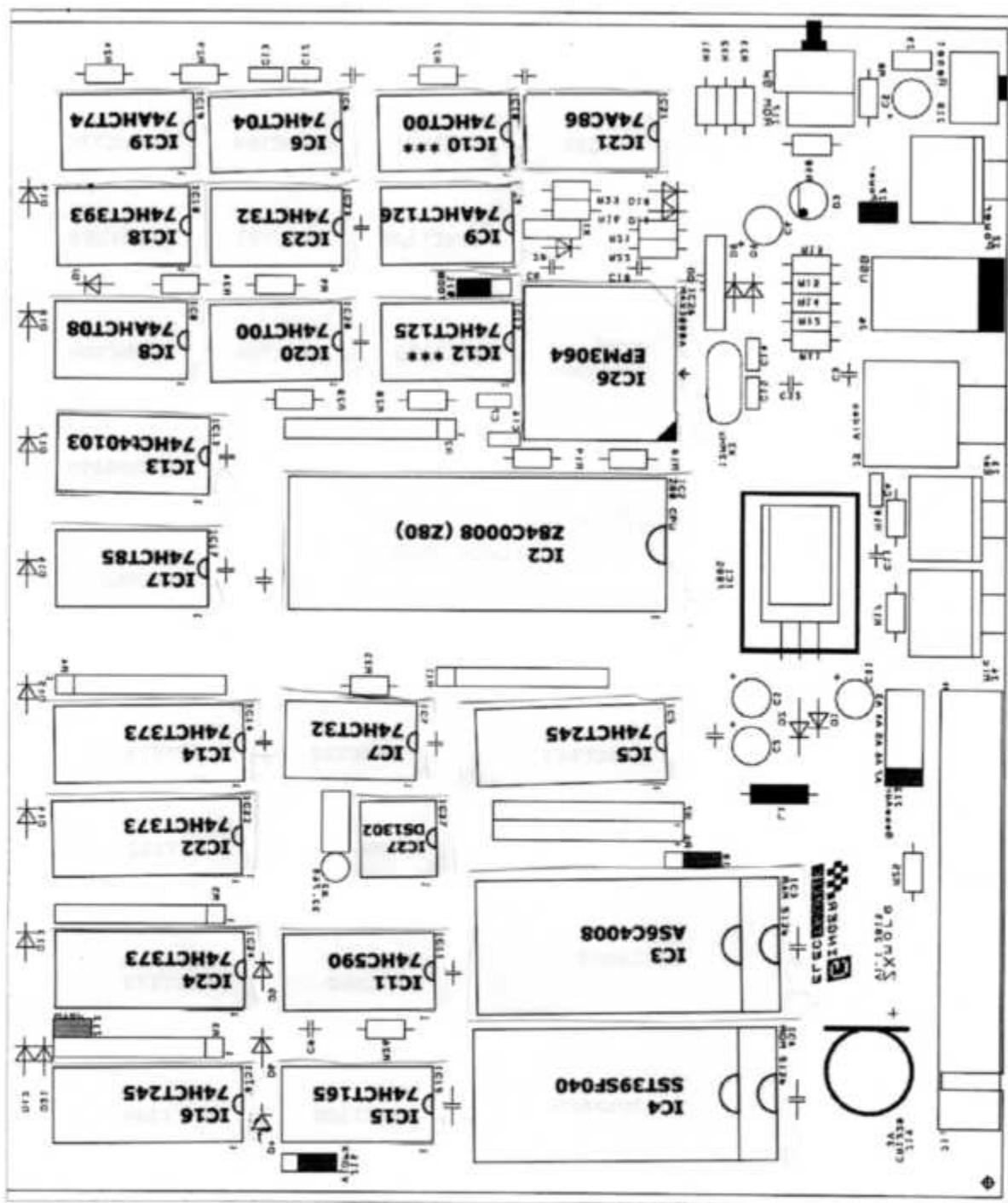
J10

IC12 ('125)



IC23 ('32)





All IC's have to be biased in the same direction with the "use" or bin I (marked with a serif or bold) in direction of the connection side of the board.
 All IC's have to be left marked with numbers for set and which position while the chassis/pins marked number is optional for setting up ZWole for NTSC video (definite PAL when not set).